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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Serial No.:

Shubhendu S.

MUKHERJEE

09/839.459

Filed:

For:

04/19/2001

Cycle Count Replication In A Simultaneous And

Redundantly Threaded

Processor

Confirmation No.: 2460

Group Art Unit:

rt Unit: 2183

Examiner:

D. H. Pan

Docket No.:

200301819-1

## RESPONSE TO OFFICE ACTION DATED MARCH 25, 2004

 $\phi$ 

Mail Stop Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Date: May 28, 2004

## **AMENDMENT**

Sir:

In response to the Office Action of March 25, 2004, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 5 of this paper.

Remarks/Arguments begin on page 12 of this paper.

124476.01/1682.37000

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